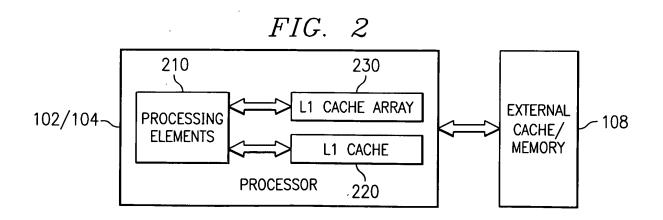
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1/6 **SERVICE** 190-**PROCESSOR** 195 SERVICE PATH FIG. 1 100 102-104 **PROCESSOR PROCESSOR** 106 SYSTEM BUS **MEMORY** 108 I/O BRIDGE -110 CONTROLLER/ **CACHE** 114 116 PCI BUS PCI BUS LOCAL 109 -**BRIDGE MEMORY** 1/0 **NETWORK** 112~ MODEM BUS **ADAPTER** 122 **GRAPHICS** 118 130 **ADAPTER** 120 PCI BUS PCI BUS **BRIDGE** 126 HARD DISK PCI BUS PCI BUS 132-**BRIDGE**

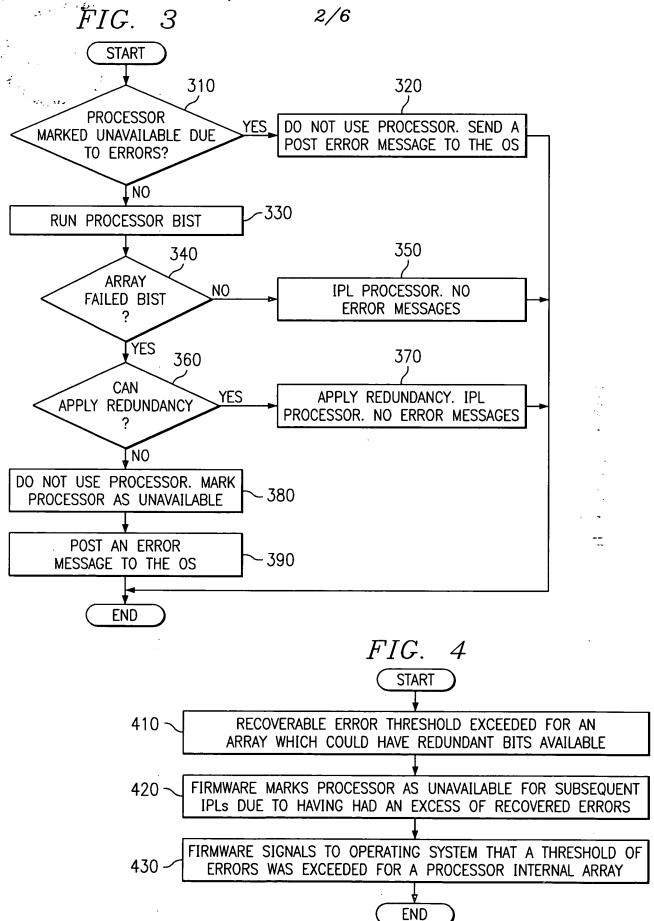


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128

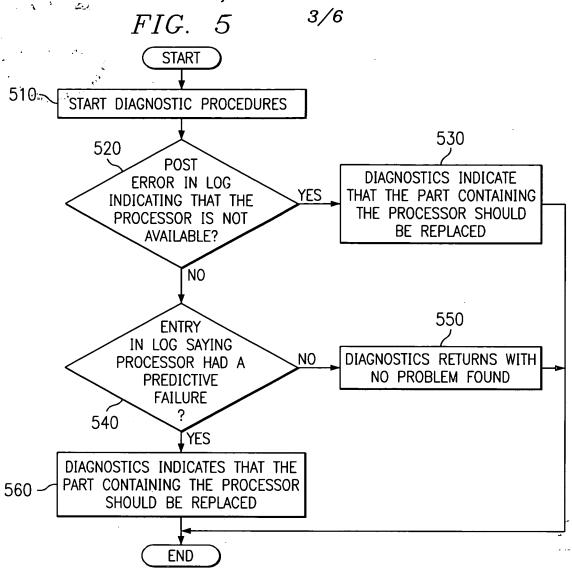
AUS920010564US1 Bossen et al.

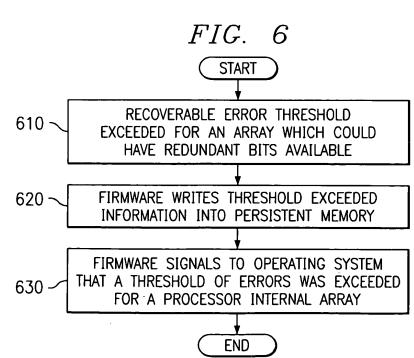
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910

930

END

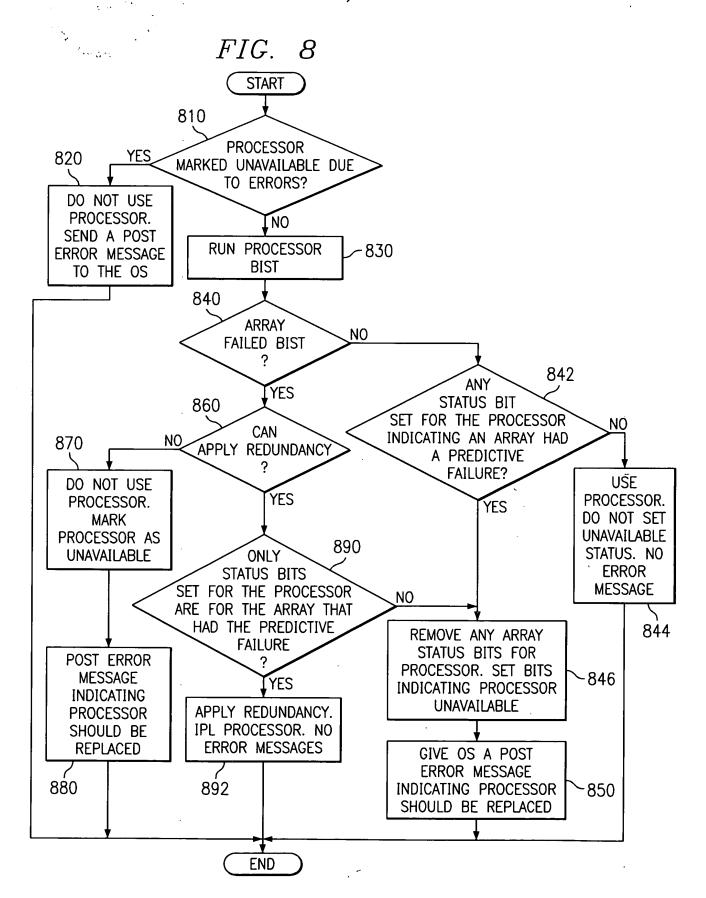
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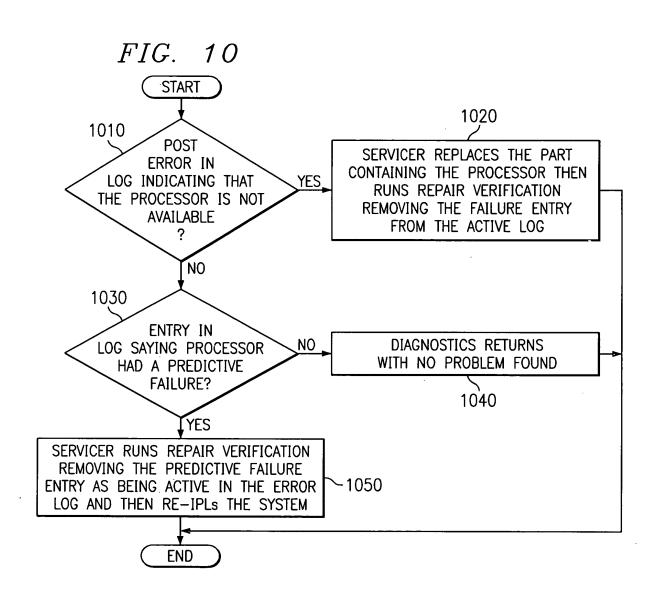
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Apparatus and Method of Repairing a Processor

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